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PATENT

DOCKET NO. 30585/10

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Korbin S. Van Dyke, et al.

Serial No.: 09/334,530

Art Unit: 2784

Filed: June 16, 1999

Examiner: unassigned

Title: PROFILING EXECUTION OF COMPUTER PROGRAMS

CERTIFICATE OF MAILING (37 C.F.R. 1.8a)

COMMISSIONER FOR PATENTS

Washington, D.C. 20231

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2. This Certificate of Mailing
3. Preliminary Amendment and Transmittal of Formal Drawings
4. Formal Drawings (32 sheets)
5. Information Disclosure Statement and Form PTO-1449
6. five references

along with any paper(s) referred to as being attached or enclosed) and this Certificate of Mailing are being deposited with the United States Postal Service on date shown below with sufficient postage as first-class mail in an envelope addressed to the: Commissioner for Patents, Washington, D.C. 20231.

Respectfully submitted,

SHEARMAN & STERLING

Dated: October 31, 2000

By: 

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PRELIMINARY AMENDMENT AND TRANSMITTAL OF FORMAL DRAWINGS

Kindly amend the application as follows.

In the Specification:

Please amend the specification as follows.

At page 5, line 2, change "schedules" to -- scheduled --.

At page 5, line 5, change "a one" to --one--.

At page 8, line 2, change "which at which at" to --which at--.

At page 9, line 28-29, change "for translating address references are" to --translates address referenced--.

At page 10, line 4, change "Preferred embodiments" to --Embodiments--.

At page 11, line 23, change "Preferred embodiments" to --Embodiments--.

At page 12, line 16, add a period at the end of the line.

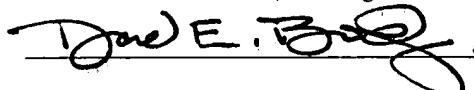
At page 14, line 1, change "Preferred embodiments" to --Embodiments--.

At page 20, lines 9-10, change "the I-cache" to --I-cache 112--.

At page 25, lines 7-12, replace the sentence "Translator 124 ... data)." with the following: --Translator 124 also assumes that all memory references are to well-behaved memory. ("Well-behaved memory" is a memory from which a load will receive the data last stored at the memory location. Non-well -behaved memory is typified by memory-mapped device controllers, also called "I/O space," where a read causes the memory to change state, or where a read does not necessarily return the value most-recently written, or two successive reads return distinct data.)--.

At page 25, line 13, replace "references" with --reads--.

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on October 31, 2000 as First Class Mail in an envelope with sufficient postage addressed to The Commissioner for Patents, Washington D.C. 20231.



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Entered in part
request a Sub
Spec.

